

App No. 10/750 960

Sheet 1 of 4

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

58268.00366

SERIAL NO.

New Continuation

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

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FILING DATE

January 6, 2003

GROUP

2175

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DD	AA	5,555,398	Sep. 1996	Raman			
	AB	5,644,784	Jul. 1997	Peek			
	AD	5,828,653	Oct. 1998	Goss			
	AE	5,423,015	Jun. 1995	Chung			
	AF	5,696,899	Dec. 1997	Kalwitz			
	AG	6,061,351	May 2000	Erimli et al.			
	AH	5,748,631	May 1998	Bergantino et al.			
	AI	6,119,196	Sep. 2000	Muller et al.			
	AJ	6,175,902 B1	Jan. 2001	Runaldue et al.			
	AK	5,473,607	Dec. 1995	Hausman et al.			
	AL	5,787,084	Jul. 1998	Hoang et al.			
	AM	5,845,081	Dec. 1998	Rangarajan et al.			
	AN	5,892,922	Apr. 1999	Lorenz			
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	AS	5,987,507	Nov. 1999	Creedon et al.			
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	AU	5,781,549	Jul. 1998	Dai			
	AV	6,041,053	Mar. 2000	Douceur et al.			
	AW	5,459,717	Oct. 1995	Mullan et al.			
	AX	5,909,686	Jun. 1999	Muller et al.			
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	BF	5,898,687	Apr. 1999	Harriman et al.			
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	BH	5,802,052	Sep. 1998	Venkataraman			
	BI	5,842,038	Nov. 1998	Williams et al.			
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	BL	5,278,789	Jan. 1994	Inoue et al.			
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	BO	5,983,223	Nov. 1999	Perlmann			
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DO	BS	WO9900936A1	Jun. 2001	WIPO					
	BT	EP0465090A1	Jan. 1992	EPO					
	BU	EP0859492A2	Aug. 1998	EPO					
	BV	EP0312917A2	Apr. 1989	EPO					
	BW	EP0853441A2	Jul. 1998	EPO					
	BX	4-189023	Jul. 1992	Japan					Abs
	BY	WO 98/09473	Mar. 1998	WIPO					
	BZ	EP0752796A2	Jan. 1997	EPO					
	CA	EP0862349A2	Sep. 1998	EPO					Abs
	CB	FR 2 725 573 - A1	Apr. 1996	France					Abs
	CD	EP0854606A2	Jul. 1998	EPO					
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CJ	WO 99/00944	Jan. 1999	WIPO						
CK	WO 99/00945	Jan. 1999	WIPO						
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CM	EP0907300A2	Apr. 1999	EPO						

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CN	"A High-Speed CMOS Circuit for 1.2-Gb/s 16 x 16 ATM Switching," Alain Chemarin et al. 8107 IEEE Journal of Solid-State Circuits 27(1992) July, No. 7, New York, US, pages 1116-1120
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CP	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
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CT	Kenneth Schultz, et al., "Architectures for Large-Capacity CAMs," Integration, the VLSI Journal, North Holland Publishing Company, Amsterdam, The Netherlands, vol. 18, no. 2, 1 June 1995
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	CW	Elm et al., "Fault Detection and Fault Localization Using IDDQ-Testing in Parallel Testable FAST-SRAMs," VLSI Test Symposium, 1994, Proceedings, 12th IEEE, 25-28 April 1994, pp 380-385

EXAMINER

David Jung

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*EXAMINER:

Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.